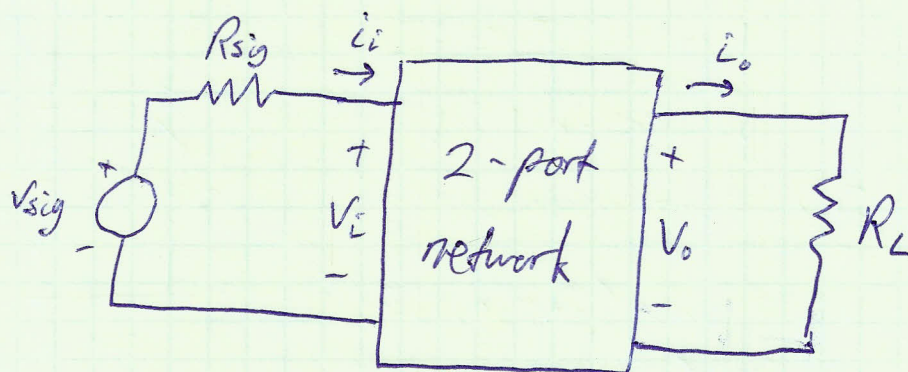


## Transistor Amplifiers

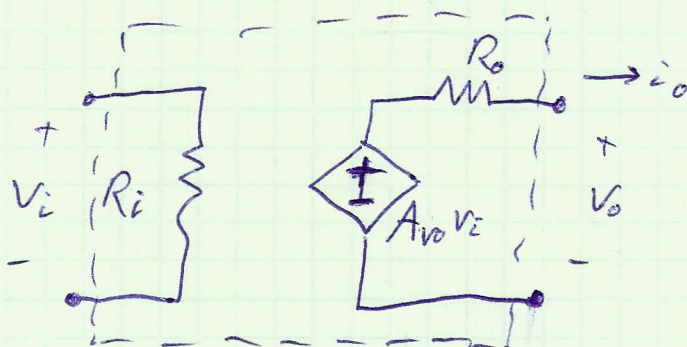
Two-port network



can be modeled with three elements:

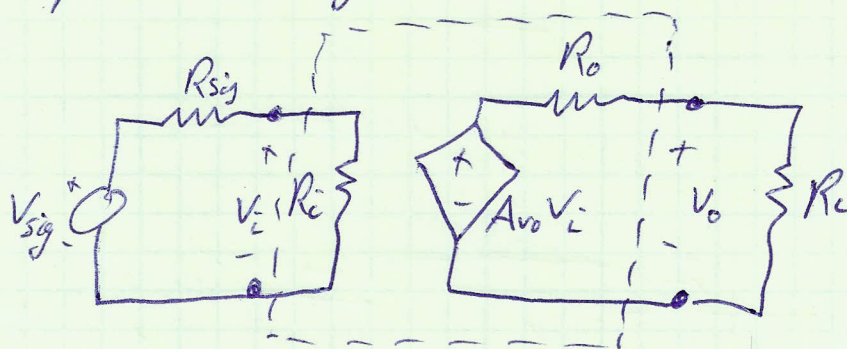
- Input resistance  $R_i = \frac{V_i}{i_i}$  (depends on  $R_L$  in general)
- Output resistance  $R_o = \left. -\frac{V_o}{i_o} \right|_{V_i=0}$
- (note - assume  $V_i$  applied directly to input port:  $V_{sig} = V_i$ ,  $R_{sig} = 0$ )
- Open-loop gain  $A_{vo} = \frac{V_{oc}}{V_i} = \frac{V_o}{V_i} \big|_{R_L \rightarrow \infty}$

## Amplifier Model



Can solve any amplifier circuit once to determine these parameters  
Similar to Thevenin theorem

Response to  $R_{sig}$  and  $R_L$ :



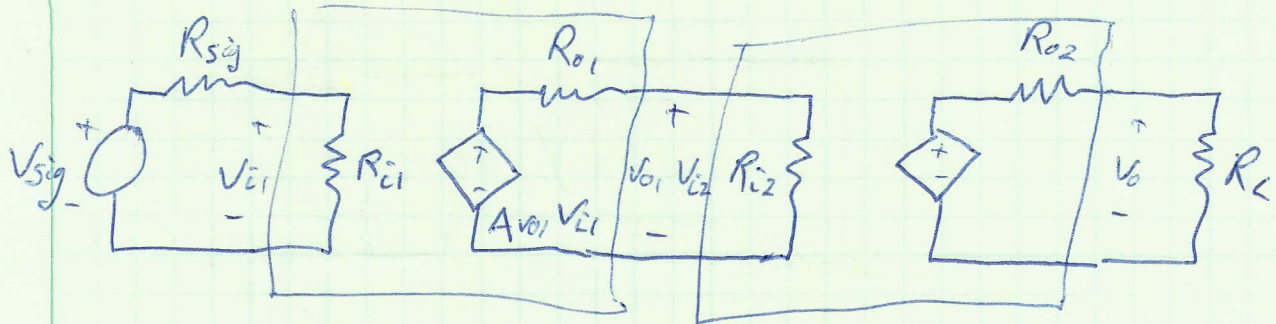
amplifier gain  $A_v = \frac{V_o}{V_i} = \frac{R_L}{R_o + R_L} A_{v0}$  (voltage divider at output)

$\frac{V_i}{V_{sig}} = \frac{R_i}{R_i + R_{sig}}$  (voltage divider at input)

$\frac{V_o}{V_{sig}} = \frac{V_i}{V_{sig}} \cdot \frac{V_o}{V_i} = \frac{R_i}{R_i + R_{sig}} \cdot A_v = \frac{R_i}{R_i + R_{sig}} \cdot A_{v0} \cdot \frac{R_L}{R_o + R_L}$

- Note that  $A_{v0}$  is the maximum amplifier gain
- to maximize  $\frac{V_o}{V_{sig}}$ , need  $R_i \rightarrow \infty$ ,  $R_o \rightarrow 0$ 
  - voltage controlled voltage source is "ideal" amplifier

## 2-stage amplifier



$$\frac{V_o}{V_{sig}} = \frac{R_{i1}}{R_{i1} + R_{sig}} \times A_{v01} \times \frac{R_{i2}}{R_{i2} + R_{o1}} \times A_{v02} \times \frac{R_L}{R_{o2} + R_L}$$

- To maximize voltage gain, input resistance of each stage must be much larger than the output resistance of the previous stage
- Often simpler to calculate  $A_v$  instead of  $A_{v0}$  ← no load  
     ↑ load is present

$$\frac{V_o}{V_{sig}} = \frac{R_{i1}}{R_{i1} + R_{sig}} \times A_{v1} (R_L = R_{i2}) \times A_{v2} (R_L = R_L)$$

- Note that  $R_i$  may depend on  $R_L$

## Analysis of Transistor Amplifiers

- Calculate Bias Conditions
- Calculate small signal response
  - find  $g_m$ ,  $r_o$ , ( $g_m$  &  $r_o$  for BJT) from bias
  - zero the bias sources
  - assume capacitors are short circuits
  - replace transistor with small signal model
  - inspect circuit:
    - if prototype circuit, use formulas for that circuit
    - otherwise, find  $R_i$ ,  $R_o$ ,  $A_v$ ,  $A_{v_o}$
- Calculate frequency response
  - we will learn how to calculate low frequency poles due to coupling caps
  - you will learn more details of frequency response in ECE102