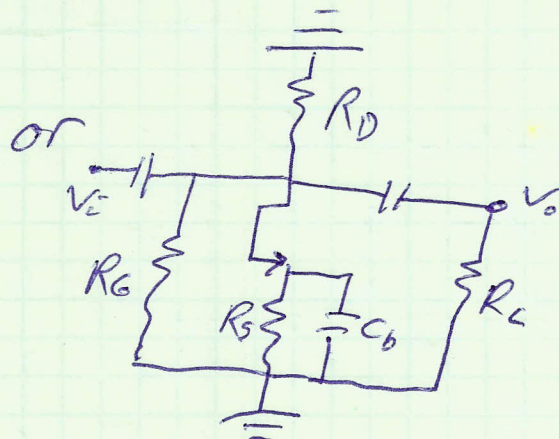
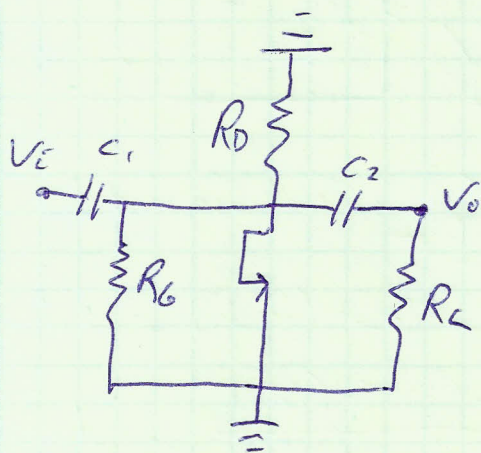
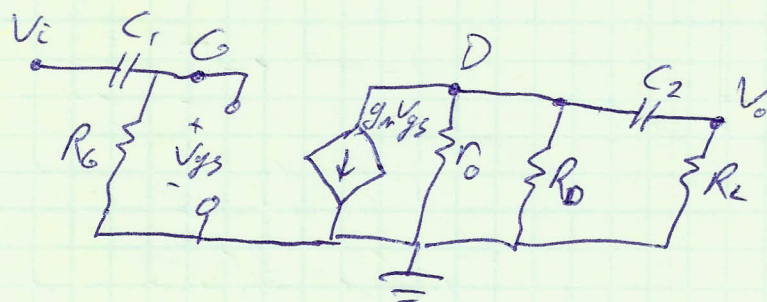


Common Source Amplifiers



bypass capacitor eliminates R_S for small signal

Small signal model:



take $R_L' = R_D || R_L$ as before

$$V_o = -g_m V_{gs} (r_o || R_L') \quad \text{note } V_{gs} = V_i$$

$$A_v = \frac{V_o}{V_i} = -g_m (r_o || R_L')$$

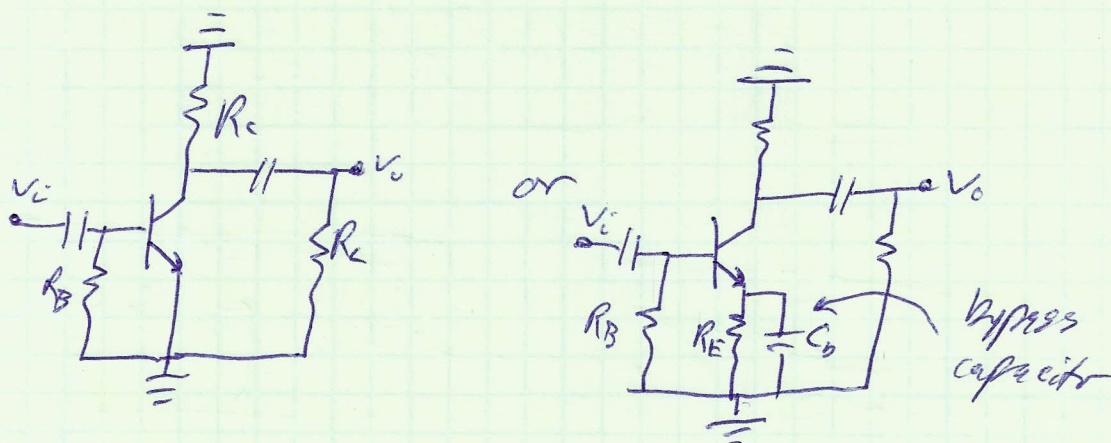
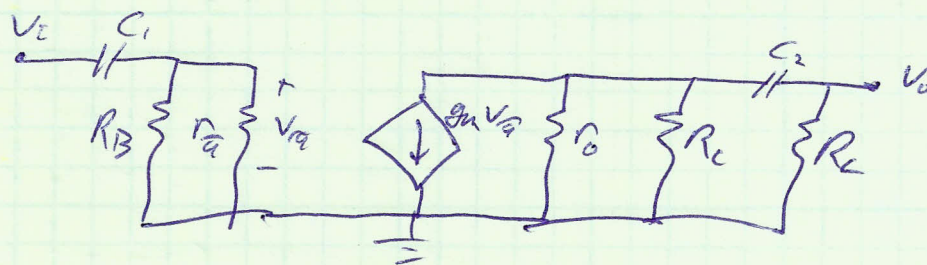
$$A_{v0} = -g_m r_o \quad (\text{set } R_L' = \infty)$$

$$R_i = R_G \quad (\text{by inspection})$$

$$R_o = r_o \quad (\text{by inspection} - \text{set } R_L' = \infty)$$

note $R_o' = R_D || R_o$ (some books, eg. Sedra & Smith define it this way)

Negative sign means
180° phase shift

Common Emitter AmplifiersSmall Signal model

$$\text{define } R_L' = R_C \parallel R_L$$

$$\text{note } V_i = V_{be}$$

$$V_o = -g_m V_{be} (r_o \parallel R_L')$$

$$A_v = \frac{V_o}{V_i} = -g_m (r_o \parallel R_L') = -\frac{r_o \parallel R_L'}{r_{\pi}}$$

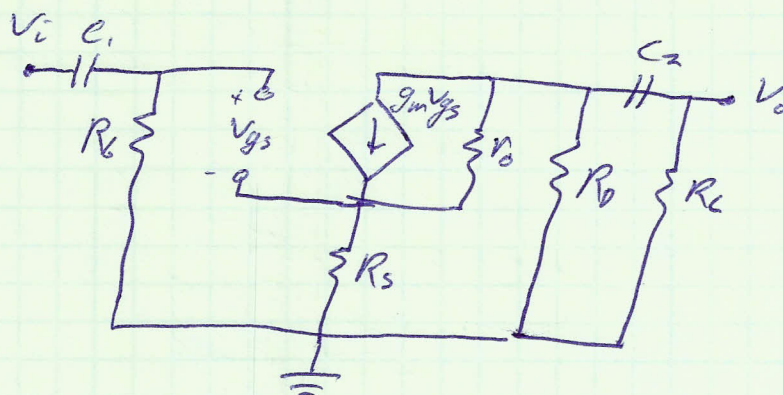
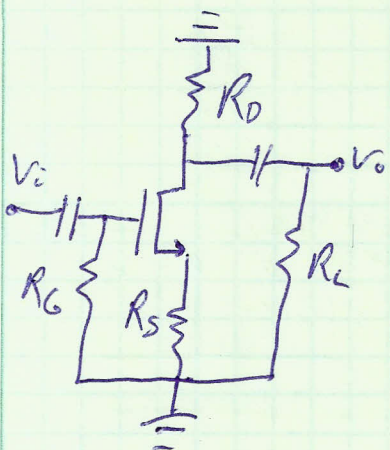
$$A_{v_0} = -g_m r_o = -\frac{r_o}{r_{\pi}}$$

$$R_i = R_B \parallel r_{\pi}$$

$$R_o = r_o \quad \text{and} \quad R_o' = R_C \parallel R_o$$

Both Common Source and Common Emitter have large open loop voltage gain
 Common Source has large input resistance (can be infinite)
 Common Emitter has medium input resistance
 Both have medium to large output resistance

Common Source with Source Resistor



at source node:

$$\frac{V_S}{R_S} + \frac{V_S - V_O}{r_o} - g_m V_{GS} = 0$$

at drain node:

$$\frac{V_O}{R_L'} + \frac{V_O - V_S}{r_o} + g_m V_{GS} = 0$$

$$\frac{V_S}{R_S} + \frac{V_O}{R_L'} = 0$$

(sum the two equations)

substitute $V_{GS} = V_i - V_S \rightarrow$ ~~find~~ eliminate V_S , find V_O , V_i

$$A_v = \frac{V_O}{V_i} = \frac{-r_o (g_m R_L')}{R_L' + r_o + R_S (1 + r_o g_m)} \approx \frac{-r_o (g_m R_L')}{R_L' + r_o + R_S r_o g_m}$$

$$A_v = \frac{-g_m R_L'}{1 + g_m R_S + R_L'/r_o}$$

$$\rightarrow A_{v0} = -g_m r_o$$

(let $R_L' \rightarrow \infty$)

if $r_o \gg R_L'$

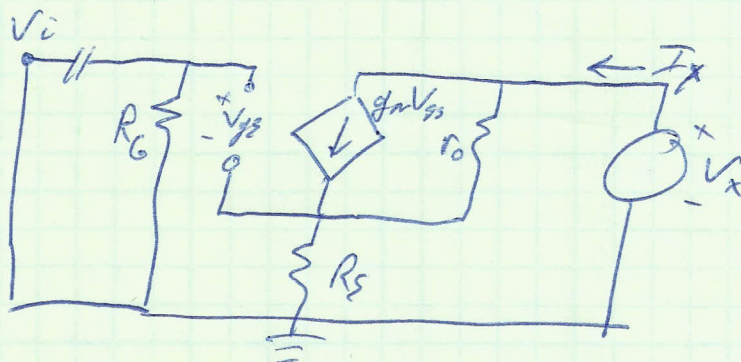
$$A_v \approx \frac{-g_m R_L'}{1 + g_m R_S}$$

gain is reduced, but less sensitive to g_m

$R_i = R_G$ by inspection

For R_o :

- take off R_L and attach V_x
- short out V_i



Current i_x should flow in R_S

current $i_x - g_m V_{gs}$ should flow in r_o

$$V_{gs} = -R_S i_x$$

KVL:

$$\begin{aligned} V_x &= r_o (i_x - g_m V_{gs}) + R_S i_x = r_o (i_x + g_m R_S i_x) + R_S i_x \\ &= i_x (r_o + g_m R_S r_o + R_S) \end{aligned}$$

$$R_o = \frac{V_x}{i_x} = r_o + R_S + g_m r_o R_S$$

$$R_o \approx r_o (1 + g_m R_S)$$

Common Emitter with Emitter Resistor

Same basic idea as for common source

$$A_v \approx \frac{-g_m R_L'}{1 + g_m R_E}$$

$$= \frac{-R_L'}{R_E + r_o}$$

$$R_L = R_B \parallel [r_\pi + (1 + \beta) R_E]$$

$$R_o = r_o \left(1 + \frac{\beta R_E}{r_\pi + R_E} \right)$$

analysis is similar to common source, but with addition of r_π

