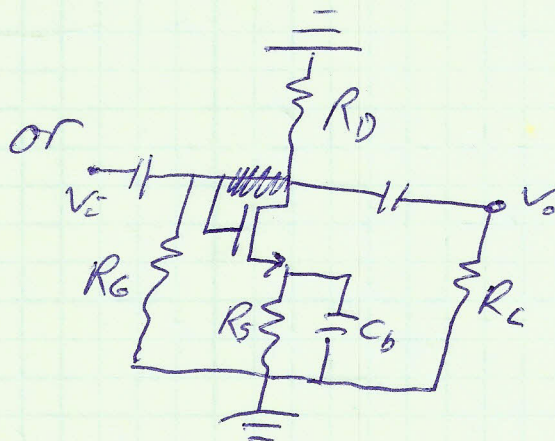
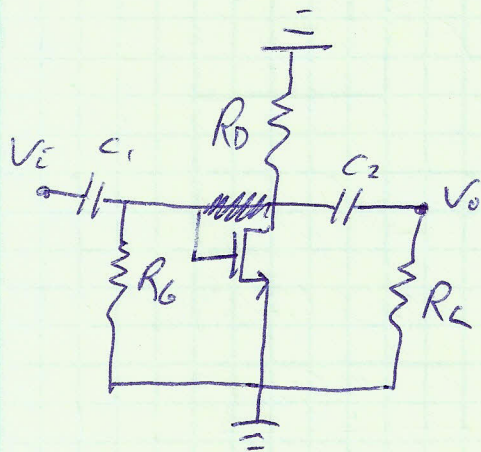
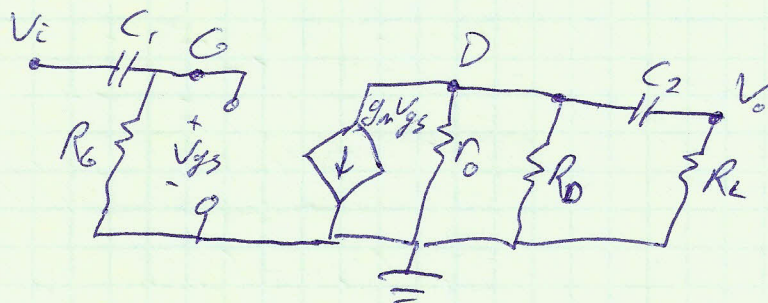


Common Source Amplifiers

bypass capacitor eliminates R_S for small signal

Small signal model:



take $R_L' = R_D \parallel R_L$ as before

$$V_o = -g_m V_{gs} (r_o \parallel R_L') \quad \text{note } V_{gs} = V_i$$

$$A_v = \frac{V_o}{V_i} = -g_m (r_o \parallel R_L')$$

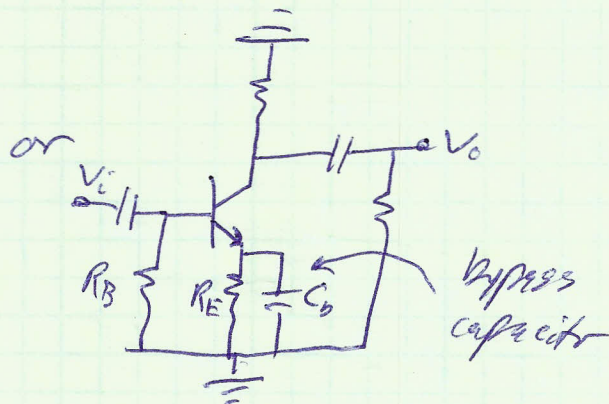
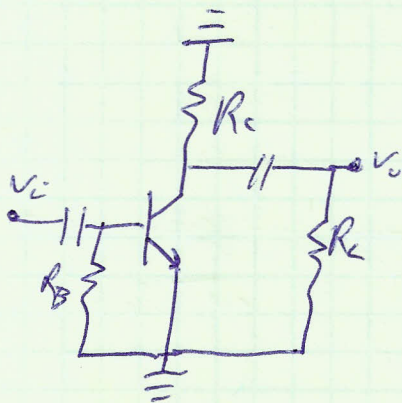
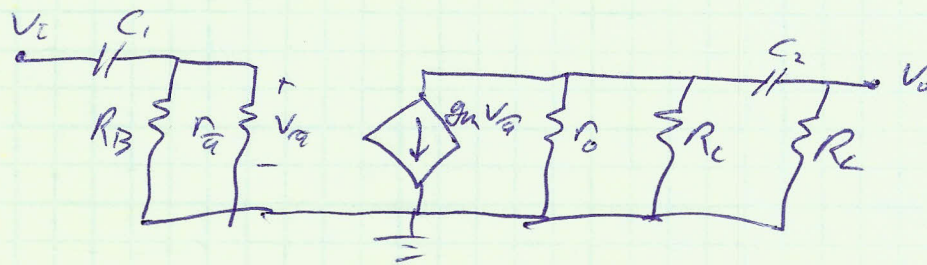
$$A_{v0} = -g_m r_o \quad (\text{set } R_L' = \infty)$$

$$R_L' = R_D \quad (\text{by inspection})$$

$$R_o = r_o \quad (\text{by inspection} - \text{set } R_L' = \infty)$$

note $R_o' = R_D \parallel R_o$ (some books, e.g. Sedra & Smith define it this way)

Negative sign means
180° phase shift

Common Emitter AmplifiersSmall Signal model

$$\text{define } R_C' = R_C \parallel R_L$$

$$\text{note } V_i = V_{\pi}$$

$$V_o = -g_m V_{\pi} (r_o \parallel R_C')$$

$$A_v = \frac{V_o}{V_i} = -g_m (r_o \parallel R_C') = -\frac{r_o \parallel R_C'}{r_o}$$

$$A_{v_o} = -g_m r_o = -\frac{r_o}{r_o}$$

$$R_i = R_B \parallel r_{\pi}$$

$$R_o = r_o \quad \text{and} \quad R_o' = R_C \parallel R_o$$

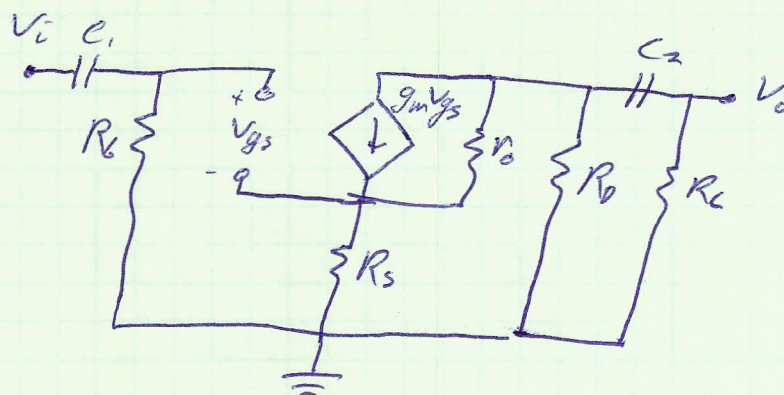
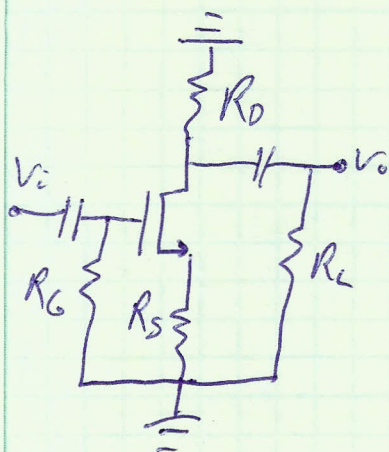
Both Common Source and Common Emitter have large open loop voltage gain

Common Source has large input resistance (can be infinite)

Common Emitter has medium input resistance

Both have medium to large output resistance

Common Source with Source Resistor



at source node:

$$\frac{V_S}{R_S} + \frac{V_S - V_O}{r_o} - g_m V_{GS} = 0$$

at drain node:

$$\frac{V_O}{R_L'} + \frac{V_O - V_S}{r_o} + g_m V_{GS} = 0$$

$$\frac{V_S}{R_S} + \frac{V_O}{R_L'} = 0$$

(sum the two equations)

substitute $V_{GS} = V_i - V_S \rightarrow$ ~~cancel~~ eliminate V_S , find V_O , V_i

$$A_v = \frac{V_O}{V_i} = \frac{-r_o (g_m R_L')}{R_L' + r_o + R_S (1 + r_o g_m)} \approx \frac{-r_o (g_m R_L')}{R_L' + r_o + R_S r_o g_m}$$

$$A_v = \frac{-g_m R_L'}{1 + g_m R_S + R_L' / r_o}$$

$$\rightarrow A_{v0} = -g_m r_o$$

(let $R_L' \rightarrow \infty$)

if $r_o \gg R_L'$

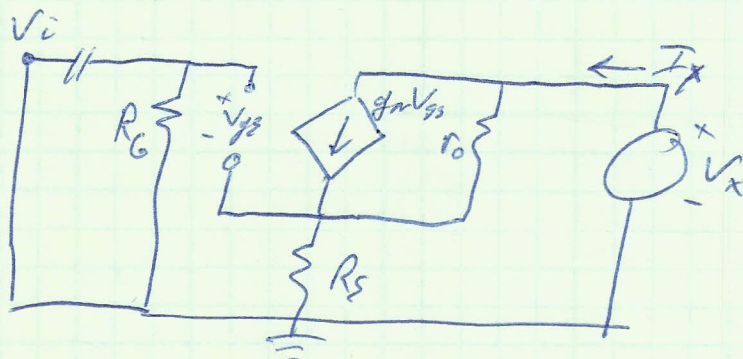
$$A_v \approx \frac{-g_m R_L'}{1 + g_m R_S}$$

gain is reduced, but less sensitive to g_m

$R_i = R_G$ by inspection

For R_o :

- take off R_L and attach V_x
- short out V_i



Current i_x should flow in R_S

current $i_x - g_m V_{gs}$ should flow in r_o

$$V_{gs} = -R_S i_x$$

KVL:

$$\begin{aligned} V_x &= r_o (i_x - g_m V_{gs}) + R_S i_x = r_o (i_x + g_m R_S i_x) + R_S i_x \\ &= i_x (r_o + g_m R_S r_o + R_S) \end{aligned}$$

$$R_o = \frac{V_x}{i_x} = r_o + R_S + g_m r_o R_S$$

$$R_o \approx r_o (1 + g_m R_S)$$

Common Emitter with Emitter Resistor

Same basic idea as for common source

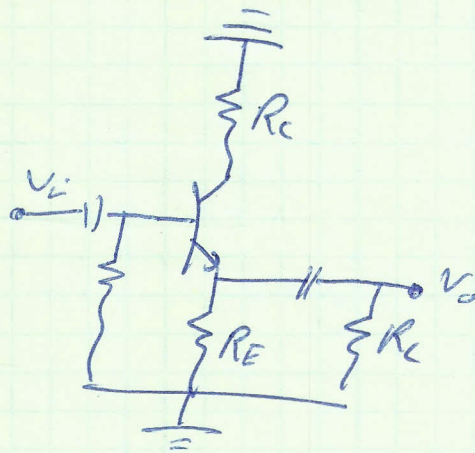
$$A_v \approx \frac{-g_m R_L'}{1 + g_m R_E}$$

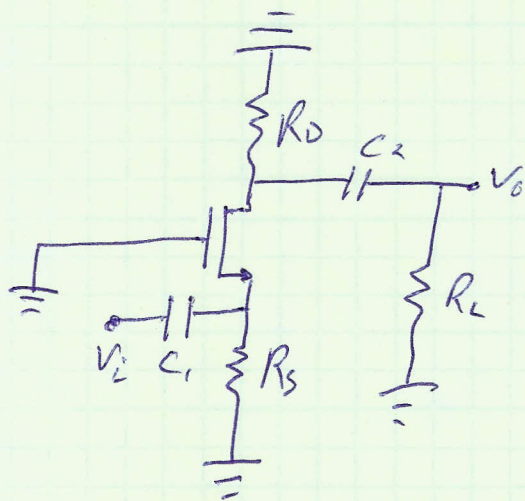
$$= \frac{-R_L'}{R_E + r_e}$$

$$R_i = R_B \parallel [r_\pi + (1 + \beta)R_E]$$

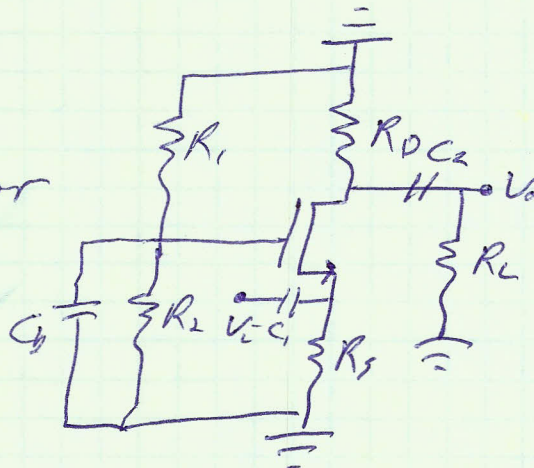
$$R_o = r_o \left(1 + \frac{\beta R_E}{r_\pi + R_E} \right)$$

analysis is similar to common source, but with addition of r_π



Common Gate Amplifiers

or



- Define $R_L' = R_D \parallel R_L$

- Note $V_i = -V_{gs}$

at node V_o :

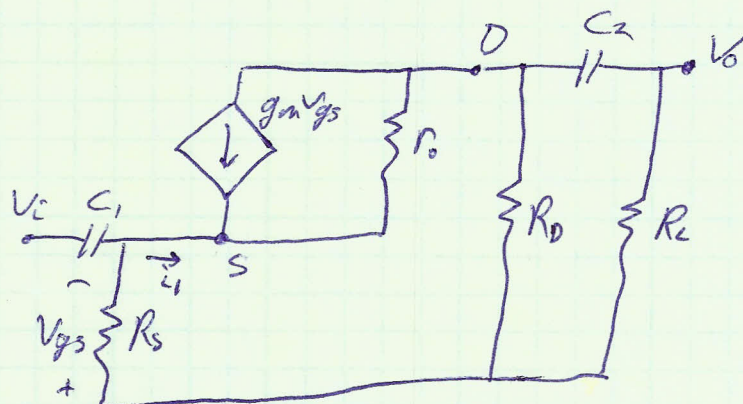
$$\frac{V_o}{R_L'} + \frac{V_o - V_i}{r_o} + g_m(-V_i) = 0$$

$$V_o \left(\frac{1}{R_L'} + \frac{1}{r_o} \right) = V_i \cdot \frac{1 + g_m r_o}{r_o}$$

$$\approx V_i \cdot \frac{g_m r_o}{r_o} = V_i g_m$$

$$A_v = \frac{V_o}{V_i} = g_m (R_L' \parallel r_o)$$

$$A_{v0} = g_m r_o$$



To find R_i : write $R_i = R_s \parallel \frac{V_i}{i_i}$

note current $i_i + g_m V_{gs}$ flows in r_o
current i_i flows in R_L'

$$V_i = (i_i + g_m V_{gs}) r_o + i_i R_L'$$

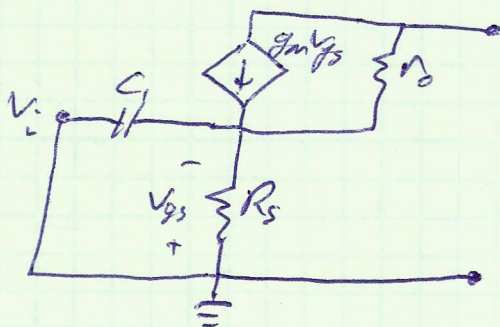
$$V_i = i_i r_o + g_m r_o V_i + i_i R_L'$$

$$\frac{V_i}{i_i} = \frac{r_o + R_L'}{1 + g_m r_o} \approx \frac{1 + R_L'/r_o}{g_m}$$

$$R_i = R_s \parallel \frac{1 + R_L'/r_o}{g_m} \rightarrow \text{note: this amplifier is not unilateral}$$

input resistance depends on load

To find R_o : take off R_L' , set $V_i = 0$



note: $V_{gs} = 0$

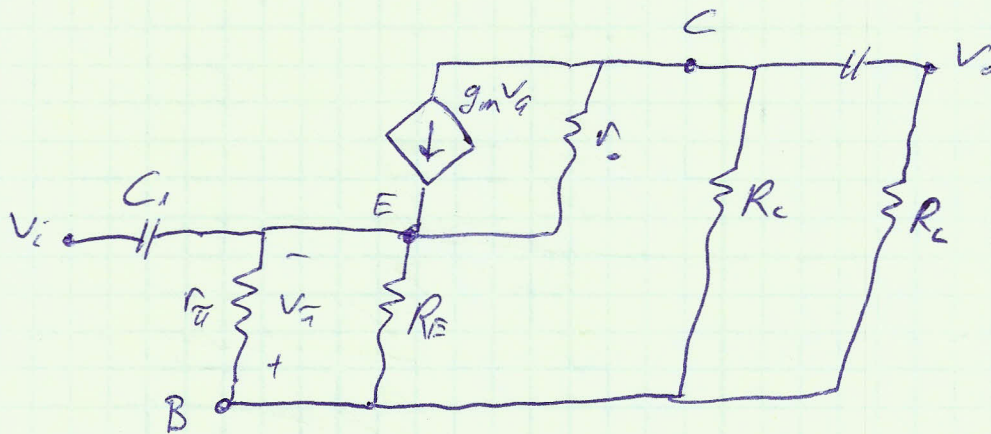
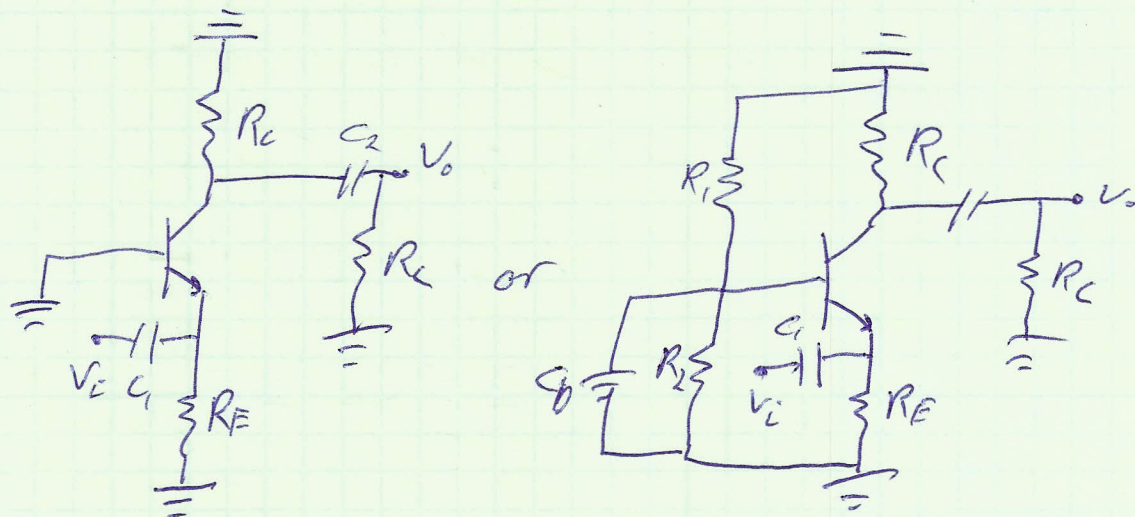
R_s is shorted

$$\rightarrow R_o = r_o$$

Characteristics:

- Large open-loop gain
 - Small input resistance
 - Medium output resistance
- } Usually used for specialized applications, or in combination with other amplifiers

Common Base Amplifiers



Note that this is identical to small signal model for common gate except that now r_o is in parallel with R_E

$$A_v = \frac{V_o}{V_i} = g_m (R_L' \parallel r_o)$$

$$A_{V_o} = g_m r_o$$

$$R_L' = R_E \parallel r_o \parallel \frac{1 + R_L'/r_o}{g_m}$$

$$R_o = r_o \quad \leftarrow \text{only change from common gate}$$